

Title of the article	Topic	Author	Affiliation	Gold Leaf
A integrated CMOS circuit for DNA hybridization detection with digital output and temperature control	Sensor Systems	Caboni Alessandra	Italy/Cagliari	1
A Linearity and Power Efficient Design Strategy for Architecture Optimization of gm-C Biquadratic Filters	analog	Crombez Pieter	IMEC/Belgium	2
The 1-V 24-GHz Low-Voltage Low-Power Current-Mode Transmitter in 130-nm CMOS Technology	digital Ics	Wang Wen-Chieh	Tawain	3
On the Use of Error Correcting and Detecting Codes in Secured Circuits	digital Ics	Vincent Maingot	Grble/France	4
Tactile Sensing Arrays for Humanoid Robots	Sensor Systems	Dahiya Ravinder S.	Italy/Genova	5
A Cascaded CT Sigma-Delta Modulator with NTF Zero and Simple Mismatch Tuning Method using Interstage Feedback	digital Ics	Sanchez-Renedo Manuel	Madrid/Spain	6
A Support Vector Machine Based Pedestrian Recognition System on Resource-Limited Hardware Architectures	Visual Sig Pro	Ghio Alessandro	Genova/Italy	7

Title of the article	Topic	Author	Affiliation	Silver Leaf
1-V 13-GHz Ultra Low Noise Amplifier for System-on-a-Chip Radiometer in CMOS 90 nm	analog	Zito Domenico	Italy/Pisa	1
A New Noise-Tolerant Dynamic Logic Circuit Design	digital Ics	Pasquale Corsonello	Italy/Rende	2
Systems-on-Chip: Use of Test Data Compression Technique to Reduce Test Time	vlsi/soc	DALMASSO Julien	LIRMM/France	3
Substrate Injection Characterization in CMOS Mixed Signal Systems on Chip	vlsi/soc	rolland du roscoat laure	Caen/France/NxP	4
A Fifth-order Continuous-Time Sigma-Delta Modulator with 62-dB Dynamic Range and 2MHz Bandwidth	analog	Wu Rong	DelphT/Nether	5
Design of a Current Mode 6-bit 100 MS/s Flash A/D Converter with 0.75 pJ/conv-lev FoM	analog	Bonizzoni Edoardo	Italy/Pavia	6
A RF circuit design methodology dedicated to critical applications	analog	cimino mikael	Bx/France	7

Removed
No show

Title of the article	Topic	Author	Affiliation	Bronze Leaf
Calculation of Sequence Lengths in MASH 1-1-1 Digital Delta Sigma Modulators with a Constant Input	analog	Hosseini Kaveh	UCC Cork/Ire	1
A Current Measurement System for Automotive Application	analog	Panhofer Harald	Infineon/Germ	2
A low power CMOS interface circuit for three-axis integrated accelerometers.	analog	Schipani Monica	Italy/Pisa	3
High density Integrated Optoelectronic Circuits for High Speed Photonic Microsystems	MEMS	Minoglou Kyriaki	Greece	4
The Factorial Delay Locked Loop: a solution to fulfill multistandard RF synthesizer requirements	analog	Majek Cedric	Bx/France	5
A low-power 6.3 GHz FBAR overtone-based oscillators in 90 nm CMOS technology	Sensor Systems	El Barkouky	Belgium/IMEC	6
Noise Reduction in Fractional-N Frequency Synthesizers with Multiphase VCO	analog	Zhipeng Ye	Cork/Ire	7